

FIG. 1

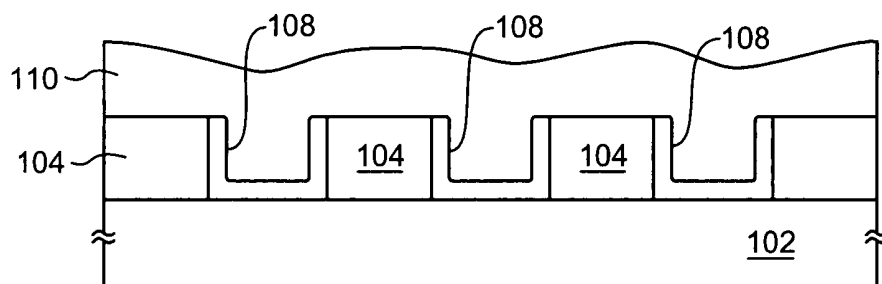


FIG. 2

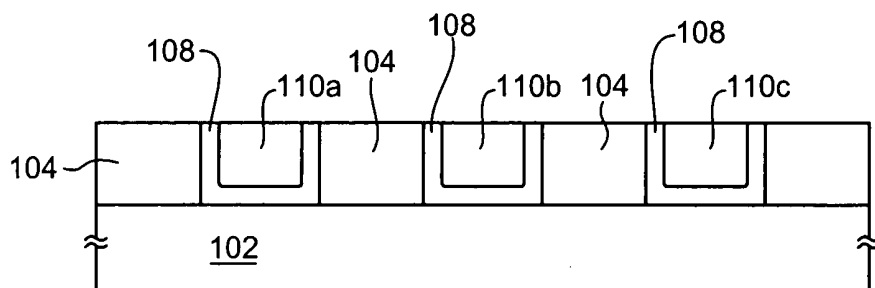


FIG. 3

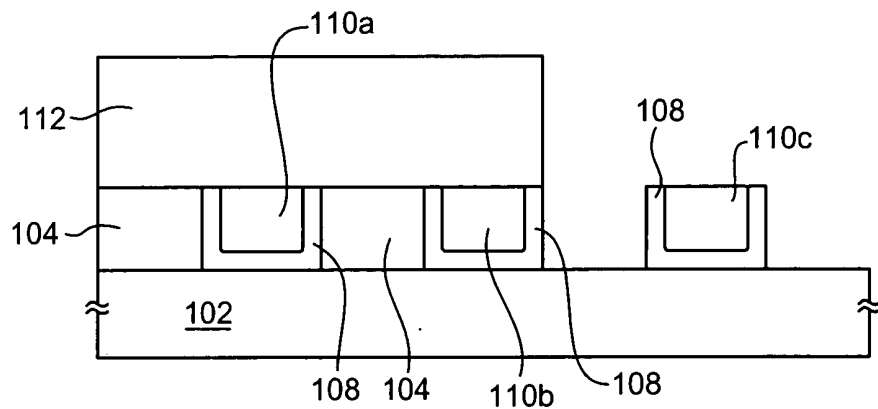


FIG. 4

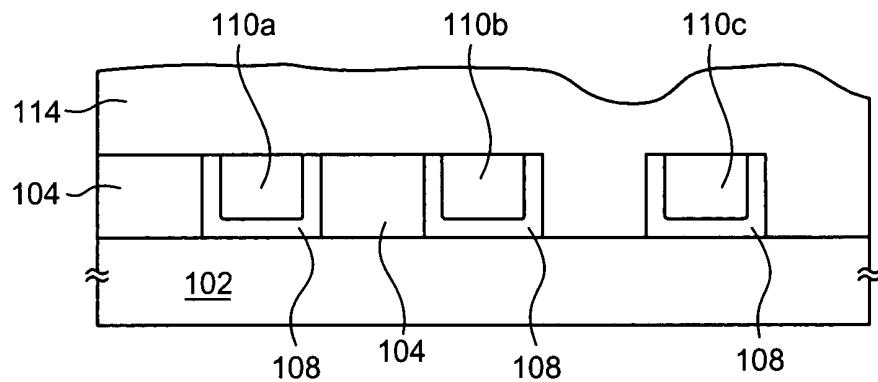


FIG. 5

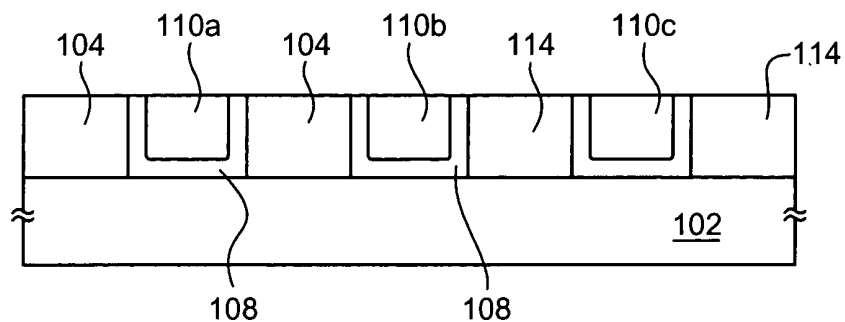


FIG. 6

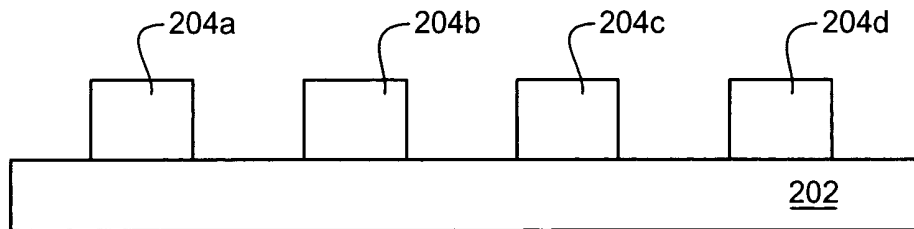


FIG. 7

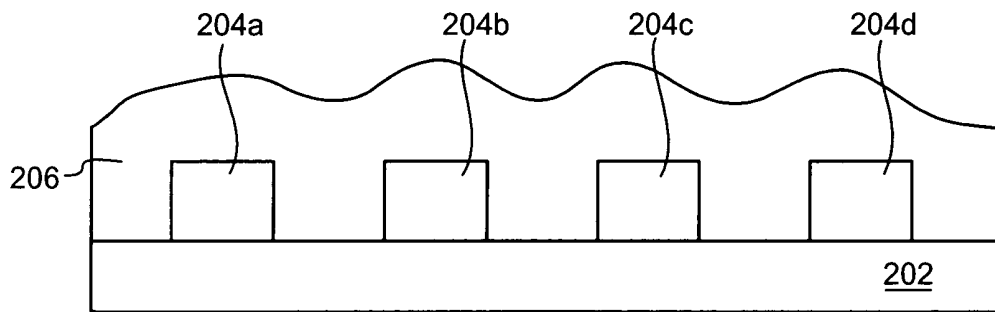


FIG. 8

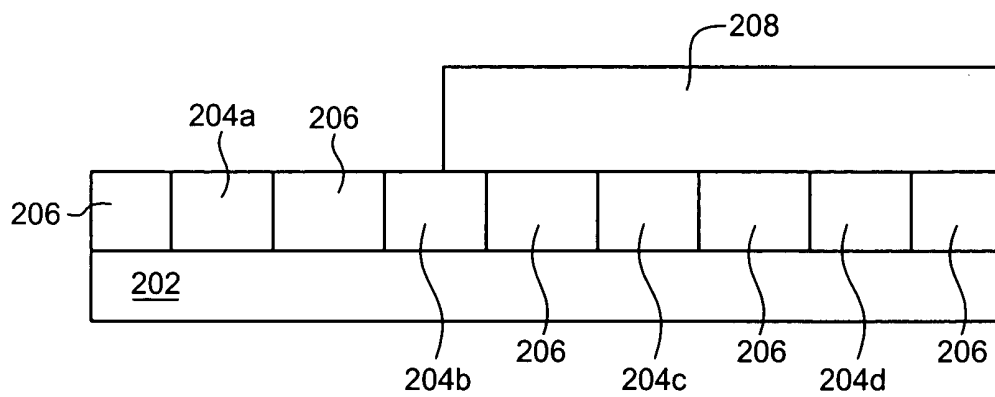


FIG. 9



Title: "In-Plane On-Chip Decoupling Capacitors and Method For Making Same"
Inventors: Chiang et al. Application No.: 09/191,930 Docket No.: 42P6459
Blakely, Sokoloff, Taylor & Zafman 303-740-1980

4/6

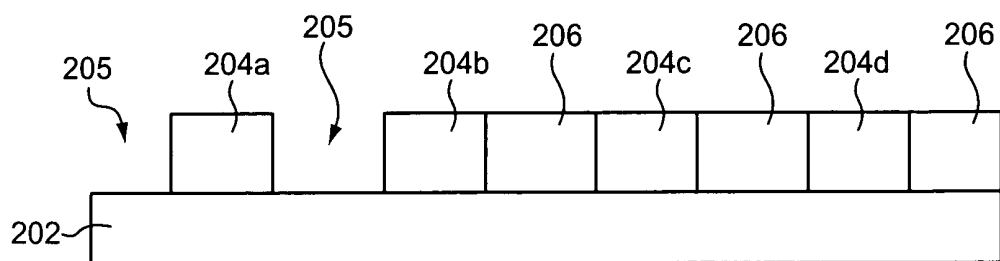


FIG. 10

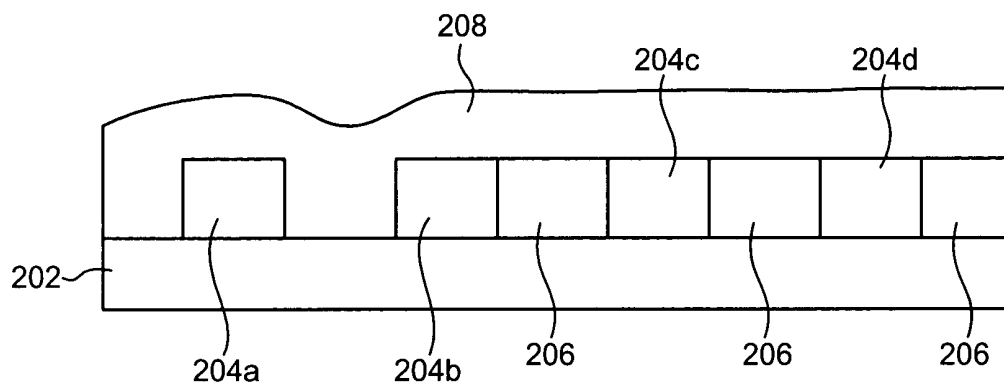


FIG. 11

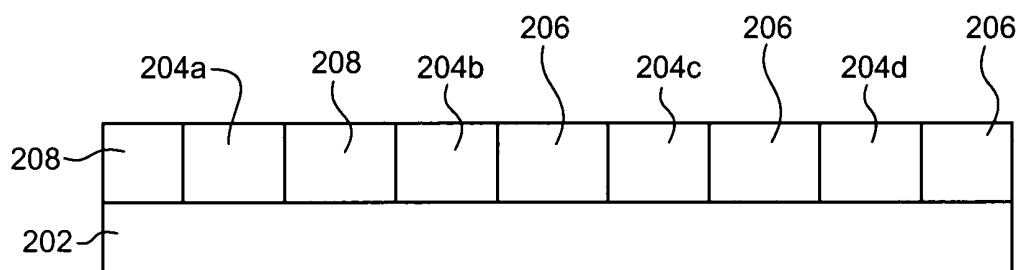


FIG. 12



Title: "In-Plane On-Chip Decoupling Capacitors and Method For Making Same"
Inventors: Chiang et al. Application No.: 09/191,930 Docket No.: 42P6459
Blakely, Sokoloff, Taylor & Zafman 303-740-1980

5/6

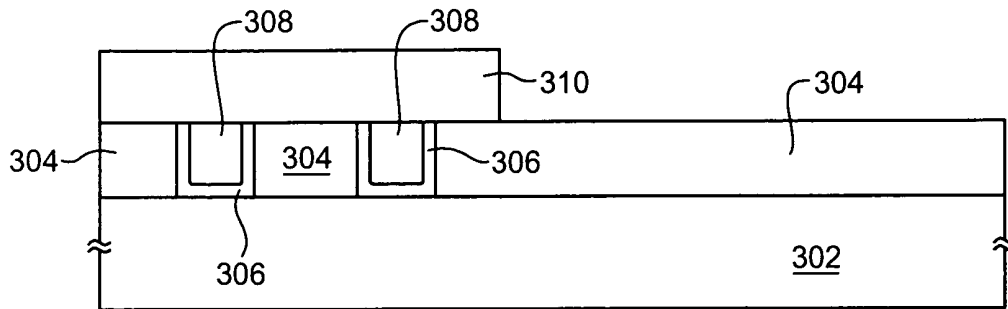


FIG. 13

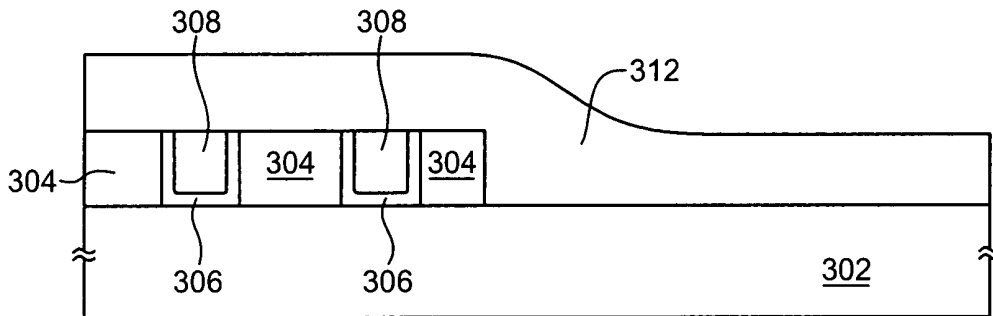


FIG. 14

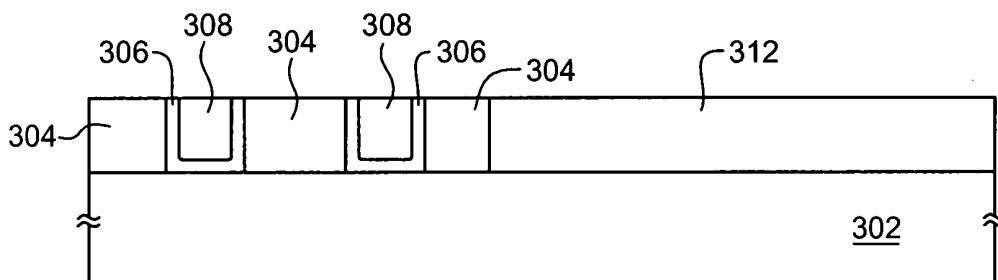


FIG. 15

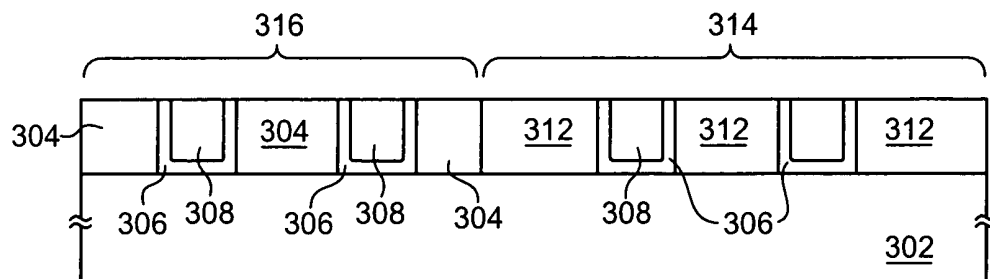


FIG. 16

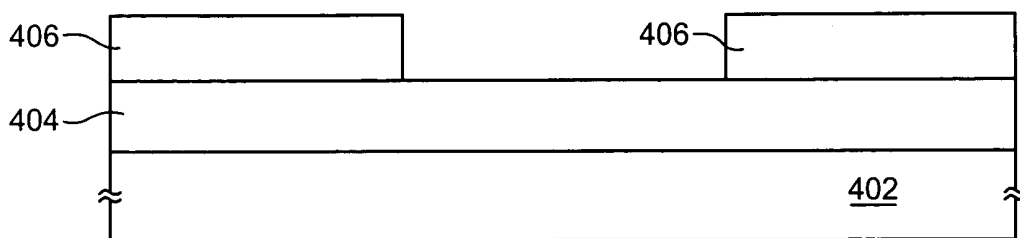


FIG. 17



FIG. 18

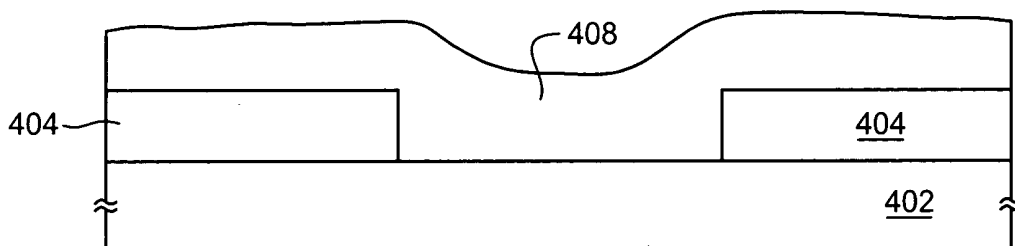


FIG. 19

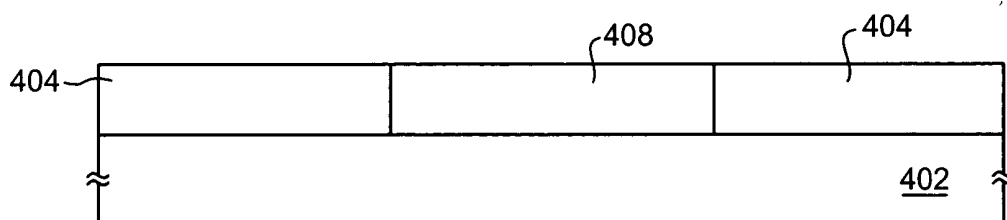


FIG. 20

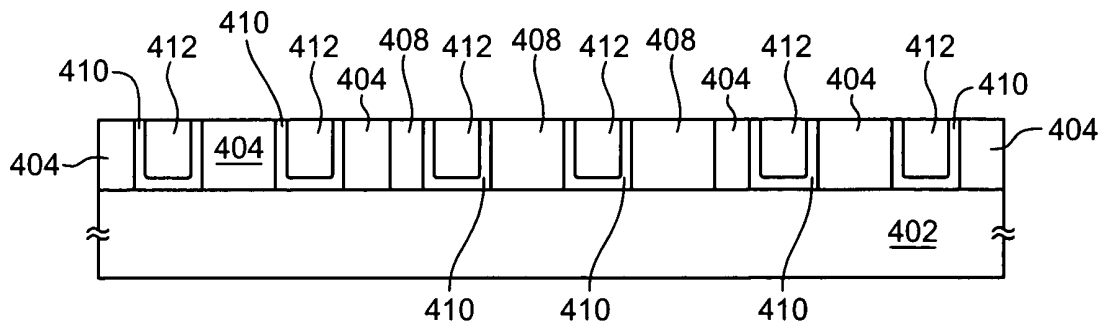


FIG. 21